L Number	Hits	Search Text	DB	Time stamp
1	1249	(717/124-138).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/16 16:02
-	2	("5564019").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 09:30
-	2	("5551013").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 13:55
-	19504441	@ad<=20000906 or @rlad<=20000906	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/16 10:27
-	209	(transfer$4 near4 data) and (((plurality or array) adj4 (processors or module$1 or element$1)) or FPGA) and emulat$5.ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 10:44
-	503	(compil$4 near4 (code or program)) and (emulat$5 or cosimulat$5) and (reprogram$6 or reconfigur$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 10:46
-	51	((block$4 near3 (code or transfer$3)) same (memory and (processor$1 or module$1 or element$1 or chip$1))) and (emulat$4 or cosimulat$4).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 11:06
-	7	((block$4 or unblock$4) near4 transfer$4) and (compil$5 and FPGA) and (emulat$4 or cosimulat$4).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 10:56
-	50	((block$4 adj3 (transfer adj3 (data near8 (memory or host)))) and (emulation or cosimulation) and ((compil$4 or decod$4) near4 code)) and (FPGA or reprogramm$5 or reconfigurab$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 18:40
-	190025	(block$4 and unblock$4 A) near5 (code or instruction$1 or module)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 11:47
-	117	(data near3 transfer$4) same ((processor$1 or element$1) near5 (host or "work station")) and (reprogrammable or FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 15:40
-	41	(((lock$4 or prohibit$4 or stop$4 or inhibit$4) or (unlock$4 or restor$4 or restart$4 or unblock$4)) near4 (transfer$3 near2 data)) and (code near4 compil$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/16 10:26
-	6	((((lock$4 or prohibit$4 or stop$4 or inhibit$4) or (unlock$4 or restor$4 or restart$4 or unblock$4)) near4 (transfer$3 near2 data)) and (code near4 compil$4)) and (FPGA or "processing elements" or "processor chips" or (array near3 processors)) and (emulat$4 or cosimulat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 15:46

–	1926	((control$4 or disabl$4) near5 (mask or bit)) and ((program or code) same compil$4) and (memory near4 (control or access))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 17:38
–	90	5021945.URPN.	USPAT	2003/04/15 16:11
–	1481	(716/4,5; 703/26).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 16:55
–	18	((716/4,5; 703/26).CCLS.) and (FPGA or ((array or network) near4 (processors or element$1 or chips))) and (emulat$4 or cosimulat$4) and (compil$4 same program)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 17:37
–	55	(((716/4,5; 703/26).CCLS.) not (((716/4,5; 703/26).CCLS.) and (FPGA or ((array or network) near4 (processors or element$1 or chips))) and (emulat$4 or cosimulat$4) and (compil$4 same program)) and 717/$)) and ((memory adj2 access$4) or (bus near3 priority)) and (control near8 (bit$1 or mask))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 17:52
–	15323	((enabl$4 or unlock$4 or (turn adj on)) and (disabl$4 or lock$4 or (turn adj off) or inhibit$4)) same ((data near3 transfer$4) ·or (memory near4 (read or write or access or store)) or (bus adj2 priority))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 17:37
–	95	(((enabl$4 or unlock$4 or (turn adj on)) and (disabl$4 or lock$4 or (turn adj off) or inhibit$4)) same ((data near3 transfer$4) or (memory near4 (read or write or access or store)) or (bus adj2 priority))) and (FPGA or ((array or network) near4 (processors or element$1 or chips))) and (emulat$4 or cosimulat$4) and (compil$4 same program)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 17:37
–	2834	((control$4 or disabl$4 or on or off or enable) near5 (mask$1 or bit$1)) and ((program or code) same compil$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/15 17:39
–	51	(maintenance adj2 bus) and (multiprocess$4 or FPGA or (host and processors)) and (((memory or bus) near3 (access or preempt$5)) same (right$1 or privile$6 or priorit$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/16 10:33
–	14	((maintenance adj2 bus) and (multiprocess$4 or FPGA or (host and processors)) and (((memory or bus) near3 (access or preempt$5)) same (right$1 or privile$6 or priorit$4))) and (host and ((maint$7 or clean$3 or recover$4 or updat$4) near4 (activit$4 or task$4)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/16 10:31
–	34	((maintenance adj2 bus) and (multiprocess$4 or FPGA or (host and processors)) and (((memory or bus) near3 (access or preempt$5)) same (right$1 or privile$6 or priorit$4))) and (emulat$4 or cosimulat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/16 11:04

—	1	((US-6134516-$ or US-6009256-$ or US-5841967-$ or US-6349392-$ or US-5410652-$ or US-6092097-$ or US-5966528-$ or US-5963746-$ or US-5551013-$ or US-5963736-$ or US-5960191-$ or US-5911059-$ or US-6122706-$ or US-5796961-$ or US-6336154-$ or US-5021945-$ or US-4885684-$ or US-6480952-$ or US-6480818-$ or US-6389379-$ or US-6480845-$ or US-5016162-$ or US-4000485-$ or US-5666515-$ or US-5592673-$ or US-6522985-$).did. or (US-5805792-$ or US-5329471-$ or US-5966534-$).did. or (EP-207320-$).did.) and (maintenance adj2 bus) and (multiprocess$4 or FPGA or (host and processors))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/16 16:00
—	32	(((maintenance adj2 bus) and (multiprocess$4 or FPGA or (host and processors)) and (((memory or bus) near3 (access or preempt$5)) same (right$1 or privile$6 or priorit$4))) and (emulat$4 or cosimulat$4)) and (FPGA or multiprocess$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/16 11:55
—	1	((((maintenance adj2 bus) and (multiprocess$4 or FPGA or (host and processors)) and (((memory or bus) near3 (access or preempt$5)) same (right$1 or privile$6 or priorit$4))) and (emulat$4 or cosimulat$4)) and (FPGA or multiprocess$5)) and (DSP or HDL or ASIC)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/16 16:02